

**NAME** : Gajendrakumar Babulal Parmar

**PERMANENT ADD :**

B/203, Dhanvihar Residency, Opp. Devnandan skay,  
Off New C.G Road, Chandkheda, Ahmedabad, Gujarat, India.

**CURRENT ADD:** Bliss Pg,Bavdhan,Pune,Maharashtra.

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**QUALIFICATION:** **B.E: Electronics & Communication** from Sankalchand Patel College of Engineering, Visnagar, Gujarat, India.

**M.E: Electronics & Communication** from Dharmsinh Desai University(DDU), Nadiad, Gujarat, India.

**PROFESSIONAL SKILLS:**

<b>Operating Systems</b>	Windows, Linux
<b>PCB Design software</b>	Cadence, Orcad, Allegro, Altium, kicad, gerb tool
<b>Hardware Simulation Tools</b>	Multisim, LTspice, Mplab
<b>Micro controller</b>	8-bit, 16-bit, 32-bit MCU and MCP
<b>Hardware platform</b>	8051, ARM7(lpc), STM32, ESP32, BLE interface
<b>Programming Language</b>	Embedded C

**Experience:**

- (1) Currently work at Ceinsys Tech ltd, From December – 2024,  
As **Sr. Embedded Hardware Engineer.**  
**Projects:** Wireless Battery Management System (Automotive)  
Farmrobo STM32 Prototype Design. (Automotive)
- (2) Worked at Acclivis Technology Pvt. Ltd. As **Sr. Hardware Design Engineer** From July-2024 Nov-2024.  
**Projects:** SMPS Design, Highspeed Board Design, DDR4, LPDD4, eMMC interfacing, Battery Management System Design RCA, Testing, Root Cause Analysis(RCA).
- (3) Worked As **Sr. Embedded Hardware Design Engineer** at Electrify Energy Pvt. Ltd. From Sept-2023 to July-2024  
**Projects:** (1) 1 Phase Smart Energy Meter Design  
(2) 3 Phase Smart Energy Meter Design

**Role and Responsibility:** Hardware Architecture Design Block Diagram, Circuit Design, Prototype Design and testing related EMI and EMC hardware certification, Testing, Root Cause Analysis(RCA) and worst case analysis (WCAs) , Design failure mode and effect analysis (DFMEA).

- (4) **Sr. Hardware Design Engineer from Dec-2022 to Sept-2023** (Contract Period), at Johnson control Hitachi(JCH). **Role and Responsibility:** Circuit Design, VAVE idea(for Cost reduction idea in Electronics Hardware ) and testing related EMI and EMC certification for any change in hardware, Functional test, Temp. Measurement of component, worst case analysis (WCAs) , Design failure mode and effect analysis (DFMEA) .
- (5) Worked as Jr. Engineer at Infiniti solutions MNC Company from september-2019 to December-2022, working experience with Maxim integrated, On semiconductor, Microchip, Renesas, NXP, Silicon Labs, Broadcom, Analog device and my role and responsibility pcb design using **Allegro for Every projects**. Schematic design, Navision(bill of material), pcb layout design, constraint manager and QC for design, Agile System.
- (6) Worked As hardware Engineer Honeywell engineer company from jan-2018 to aug- 2019. **Role and Responsibility:** Circuit Design and testing, Functional test, Design failure mode and effect analysis (DFMEA).
- (7) Worked as Associate Technical Support At TechM from Nov-2016 to Dec-2017.
- (8) 4 months experience in ISRO as contract Engineer Project(contract) based from 03- 03-2016 to 30-06-2016.
- (9) Worked as Engineer Honeywell engineers company from Dec-2012 to March-2016.
- (10) 1 year and 7-month Teaching experience an Assistant Professor in degree engineering College from 16-06-2011 to 07-12-2012, SPCE, VISNAGAR and MEC, BASNA Engineering college.

- Projects:**
- (1) IMAX8 Processor Based Camera Interfacing, LPDD4, SD Card Interfacing, Highspeed Board Design, Signal Integrity (SI) and Power Integrity (PI), Thermal Analysis, BMS (Battery Management System) Design and Testing, SMPS Design, Analog, Digital and Mixed Signal Design.
  - (2) 1Phase, Dain Rail, 3 Phase Smart Energy Meter Design Block diagram, Schematic and Layout Design Review, **Isolated and Non Isolated SMPS Design** As per Standard. Energy Meter Testing, Root Cause Analysis. **Quectel** Communication Module Design And Testing, SMPS Design.
  - (3) **Bluetooth V5.1** Transceiver Module 2.4GHz ~ 2.4835GHz (DA14531MOD) Integrated with Energy meter.
  - (4) I have exp. of **high-speed PCB layout design, Analog, Digital and Mix**

**signal design**, Complex Schematic design, footprint creation, Bill of Material, **Artwork (Gerber)**, knowledge of **EMI/EMC**, DFM, ADC and DAC Design, Hardware design testing and knowledge of CRO(Oscilloscopes), Multimeter, function generator.

(5) **Title: Comparison between SPI Protocol and IIC Protocol**

My Research Project based on Comparison between Serial Peripheral Interface (**SPI**) Protocol and Inter Integrated Circuit (**I<sup>2</sup>C, IIC or I2C**) For that choose **ARM7** Processor and **NXP LPC 2368** Philips kit.

(6) **Title:** Temperature measurement using LM35 sensor

(7) **Title:** Ultrasonic Range Finder Using PIC 16f84 Controller

(8) **Title:** UART (Universal Asynchronous Receiver Transmitter)

(9) **Title:** solar based sun tracking system, in that I have used LDR sensor for tracking and Arduino controller.

(10) **Title:** Digital clock with integrated alarm

(11) **Title:** stepper motor

(12) **Title:** Lcd display

(13) **Title:** Led display

(14) **Title:** Power supply design

(15) **Title:** sine, square and triangular wave generator.

(16) **Title:** Power supply Circuit design.

I have also guided to the students in various projects. I know about the ARM Processor, NXP LPC 23xx (low power consumption) kit and related its application.

**Extra activities:** (1) Research Paper Publications at International conference and National conference for SPI (Serial Peripheral Interface) and IIC(Inter integrated Circuit) Protocol

(2) Participate in Robotics Competition.

(3) Participate in Electronics Model Presentation

(4) **Six Sigma** White belt

(5) **Advanced Battery Management System Design** from Udemy and from Diyguru.

(6) **Highspeed Design** Certification from Udemy

(7) **Project Management Certification** from Udemy and **IIM Rohtak**

**AREA OF INTERESTS:** Electronics Hardware Design

**HOBBIES:** Reading books.

**PERSONAL PROFILE**

**Name** : Parmar Gajendrakumar Babulal

**Date of Birth** : 16Apr'85

**Sex** : Male

**Nationality** : Indian

**Marital status** : Married

**Languages Known** : English, Hindi and Gujarati.